



IN 09/880,700

PATENT

2131
#12
SP
9-25-03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: R. Takahashi
Serial No.: 09/880,700
Filed: 06/13/2001
Title: METHOD AND APPARATUS FOR CREATING A MESSAGE DIGEST
USING A MULTIPLE ROUND ONE-WAY HASH ALGORITHM

Examiner: Unknown
Group Art Unit: 2131
Docket: 4224-19US1

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on this 12 day of Sept 2003.

Bruce T. Neel

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SEP 17 2003

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INFORMATION DISCLOSURE STATEMENT

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants further request that a copy of the 1449 form, initialed by the Examiner to indicate that all listed citations have been considered, be returned with the next official communication. Under 37 C.F.R. §1.97(b)(3), it is believed that no fee or certificate is required with this Information Disclosure Statement.

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.


Respectfully submitted,

R. Takahashi

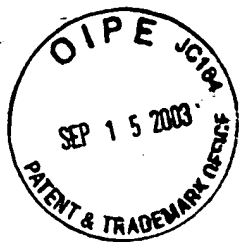
By their Representatives,
GAMMAGE & BURNHAM, PLC
Two North Central Avenue, 18th Floor
Phoenix, Arizona 85004
(602) 256-0566

Date: _____

9-12-03


By: Bruce T. Neel

Reg. No. 37,406



Sheet 1 of 1

Form 1449*

Atty. Docket No.: 4224-19US1

Serial No. 09/880,700

**INFORMATION DISCLOSURE STATEMENT
BY APPLICANT**
(Use several sheets if necessary)

Applicant: R. Takahashi

Filing Date: 06/13/2001

Group: 2131

U.S. PATENT DOCUMENTS

**EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	

FOREIGN PATENT DOCUMENTS

**EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

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OTHER DOCUMENTS

****EXAMINER INITIAL** (Including Author, Title, Date, Pertinent Pages, Etc.)

DAEMEN J ET AL: "Fast Hashing and stream Encryption with Panama"
pp. 1-15

BOSSELAERS A ET AL: "SHA: A Design for Parallel Architectures?"
Katholieke Univesiteit Leuven, Dept. Electrical Engineering-ESAT
Kardinaal Mercierlaan 94, B-3001 Heverlee, Belgium, 25 February 1997,
pp. 1-15.

BOSSELAERS A: "Even Faster Hashing on the Pentium" Katholieke
Univesiteit Leuven, Dept. Electrical Engineering-ESAT Kardinaal
Mercierlaan 94, B-3001 Heverlee, Belgium, 13 May 1997, updated
13 November 1997

International Search Report mailed September 12, 2002 from European
Patent Office for counterpart PCT application No. PCT/US02/18637.

Examiner

Date Considered

* Substitute Disclosure Statement Form (PTO-1449)

****EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP 609;
Draw line through citation if not in conformance and not considered. Include copy of this form with next
communication to applicant.